



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re th	he application of:)		
	Singh et al.)	Group Art Unit:	J
Applic	cation No. 10/633,021)	Examiner: Unas	J
Filed:	7/31/2003)	Atty. Docket No.	NVIDP235/ P000846
For:	PAD OVER ACTIVE CIRCUIT SYSTEM AND METHOD WITH FRAME SUPPORT STRUCTURE))))	Date: November	er 6, 2003

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313-1450 on November 6, 2003.

Signed:

Erica L. Farlow

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR §§1.56 AND 1.97(b)

Commissioner of Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Application 10/633,004, filed 07/31/2003, may be material to examination of the above-identified patent application. Applicants identify this application in compliance with their duty of disclosure pursuant to 37 CFR §§1.56 and 1.97. The Examiner is requested to make this statement an official record in this application. However, IT IS RESPECTFULLY REQUESTED THAT THE APPLICATION SERIAL NUMBER AND FILING DATE NOT BE IDENTIFIED ON ANY PATENT ISSUED FROM THE ABOVE-IDENTIFIED APPLICATION under MPEP §609C(2) and §609D.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

This Information Disclosure Statement is believed to be filed before the mailing date of a first Office Action on the merits. Accordingly, it is believed that no fees are due in connection with the filing of this Information Disclosure Statement. However, if it is determined that any fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 50-1351 (Order No. NVIDP235/P000846).

Respectfully submitted,

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